

block to the TV camera outputs and through the videosignal standart convertor and RAM - on the PC input and the synchronisator is connected through its control input - on the synchronisation output at least of last TV camera and through their control outputs - on the clock input of the ADC block, on the address inputs of the indicated corector and on the address and control synchro-inputs of the videosignal standart convertor.

13 э.п.ф.; 10 ил. (Fig.2)

00242210-071700